

FILEID**MCDEF

G 1

MM	MM	CCCCCCCC	DDDDDDDD	EEEEEEEEE	FFFFFFFFF
MM	MM	CCCCCCCC	DDDDDDDD	EEEEEEEEE	FFFFFFFFF
MMMM	MMMM	CC	DD	EE	FF
MMMM	MMMM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CC	DD	EE	FF
MM	MM	CCCCCCCC	DDDDDDDD	EEEEEEEEE	FF
MM	MM	CCCCCCCC	DDDDDDDD	EEEEEEEEE	FF

.....
.....

MM	MM	DDDDDDDD	LL
MM	MM	DDDDDDDD	LL
MMMM	MMMM	DD	DD
MMMM	MMMM	DD	DD
MM	MM	DD	DD
MM	MM	DD	DD
MM	MM	DD	DD
MM	MM	DD	DD
MM	MM	DD	DD
MM	MM	DD	DD
MM	MM	DD	DD
MM	MM	DD	DD
MM	MM	DDDDDDDD	LLLLLLLL
MM	MM	DDDDDDDD	LLLLLLLL

CL
mo/*
/*
/*
/*
/*
/* ag
/*
/*
/* CL
CL
CL
CL
en/*
CL
en/*
CL

Version: 'V04-000'

```
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*****
```

Define Machine check stack parameters.

```
$STRUCT MC  
F      BYTCNT,L  
F      TYPECODE,L  
F      P1,L  
F      P2,L  
F      PC,L  
F      PSL,L  
E
```

Define memory controller CSRs.

```
$STRUCT MEM  
F      CSR0,L  
F      CSR1,L  
F      CSR2,L  
E
```

```
$STRUCT CSR0  
V<M  
ECCSYND,7      ; ECC error syndrome.  
2  
ERRPFN,15      ; PFN of error.  
>  
E
```

```
$STRUCT CSR1
V<M
ECCMAINT,7
,18
ECCDISAB,
DIAG,
MME,
CRDENAB,
TBPAR,
CRD,
,1
>
E
```

; ECC maintenance check bits.
; Disable ECC.
; Diagnostic mode.
; Memory mapping enable.
; Enable CRD interrupts.
; Force translation buffer parity.
; Corrected Read Data error.

```
$STRUCT CSR2
V<M
MEMMAP,16
,8
CHIPSIZE,1
,7
>
E
```

; One bit for each 128K/512K (depending on
; chip size).
; 0 => 128 Kbytes/bit, 1 => 512 Kbytes/bit.

0391 AH-BT13A-SE
VAX/VMS V4.0

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